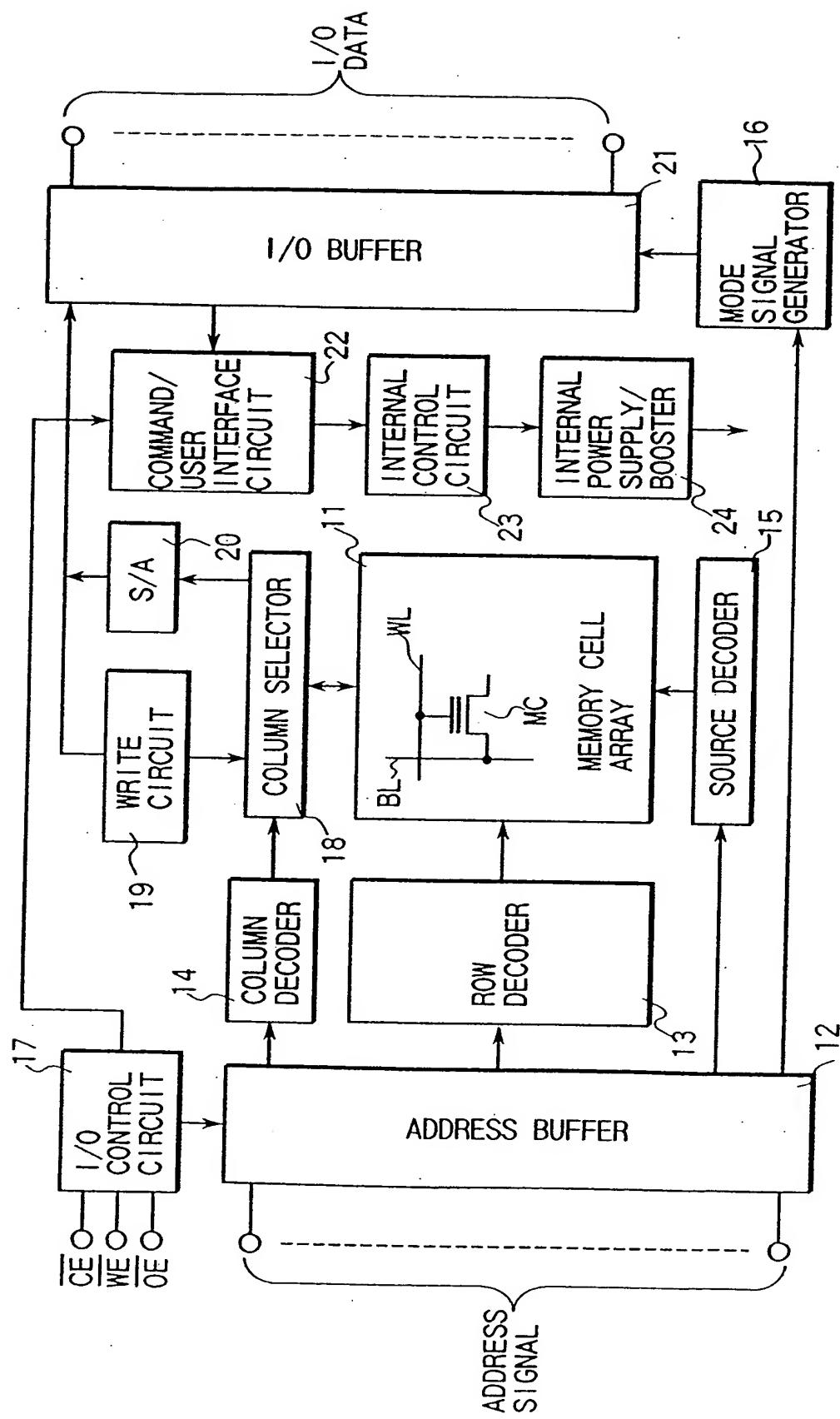


FIG. 1



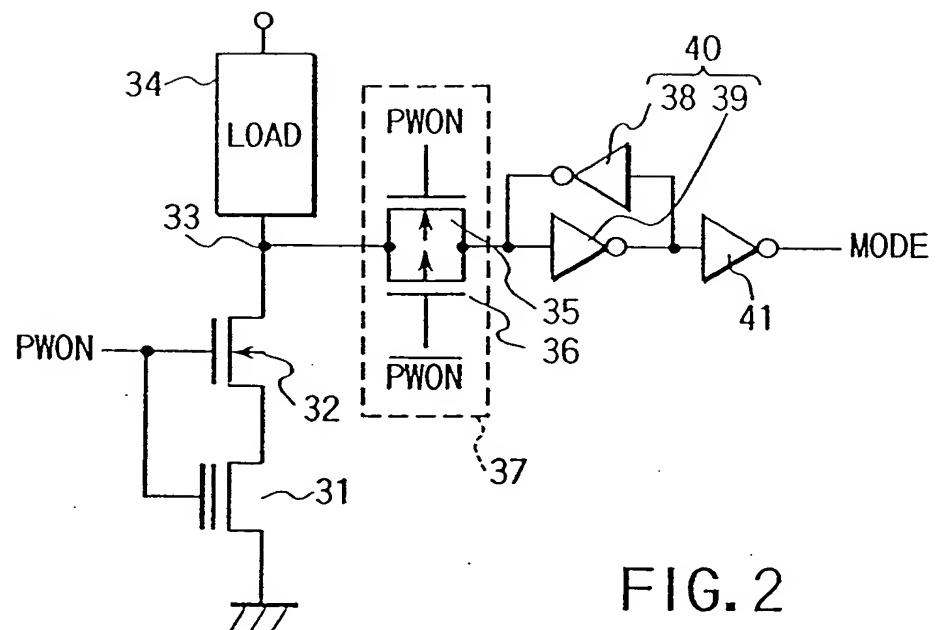


FIG. 2

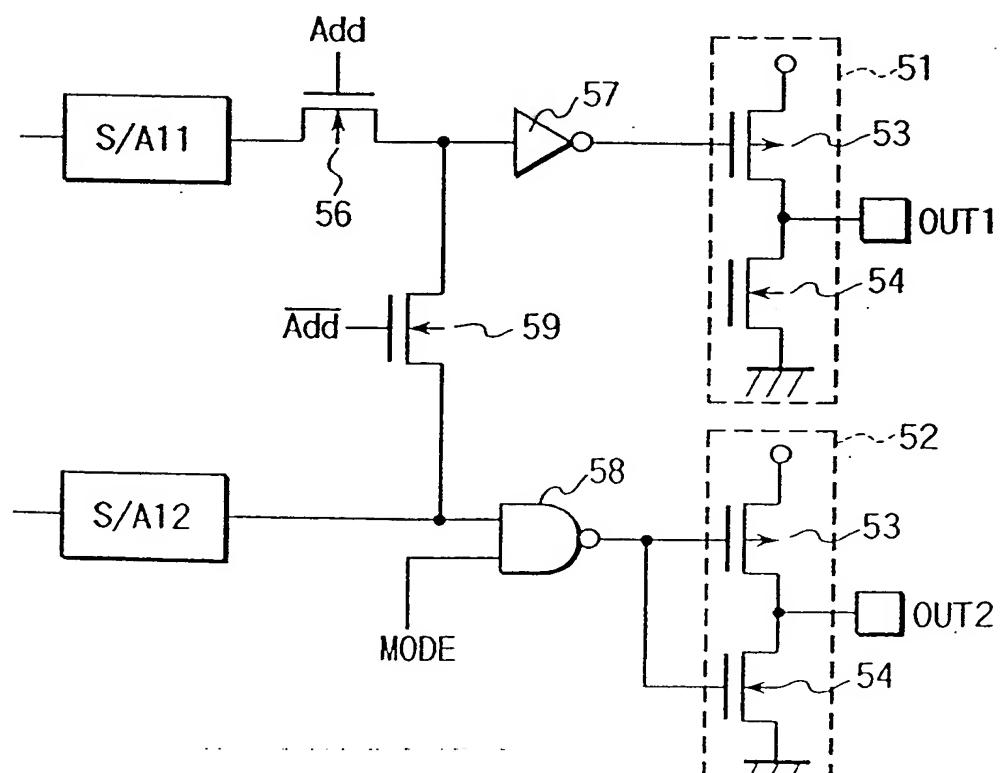


FIG. 3

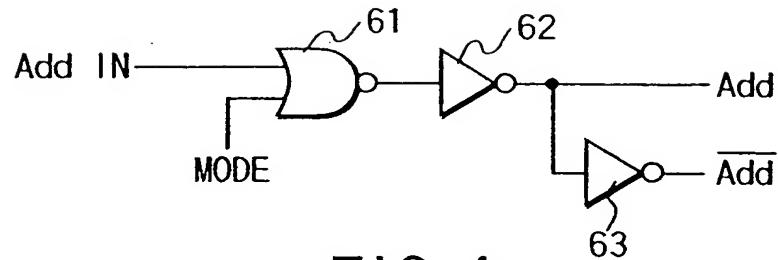


FIG. 4

	V <sub>g</sub>	V <sub>d</sub>	V <sub>s</sub>
WRITE	10V	6V	0V
ERASE	-7V	OPEN	6V
READ	5V	1V	0V

FIG. 5

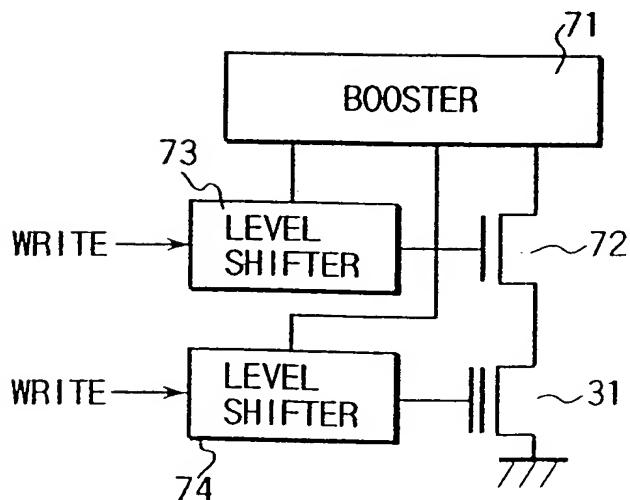


FIG. 6A

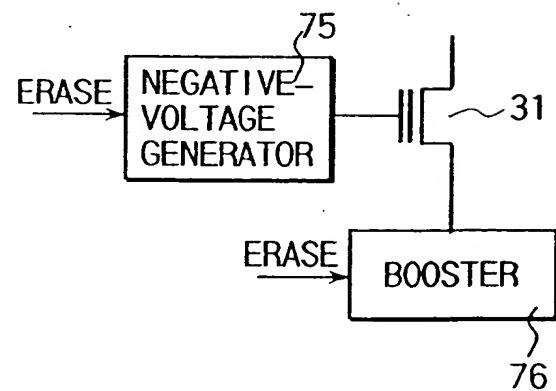


FIG. 6B

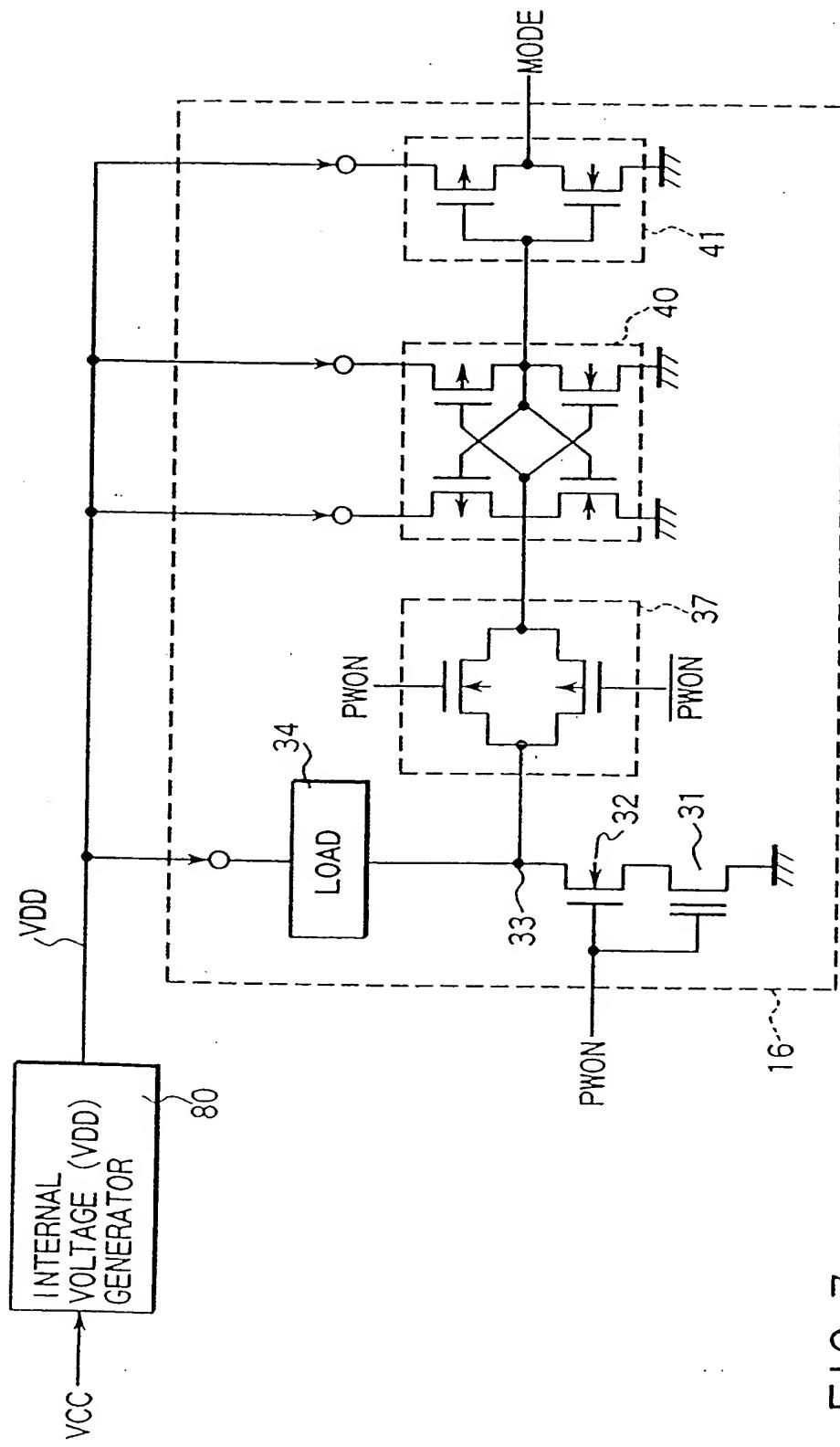


FIG. 7

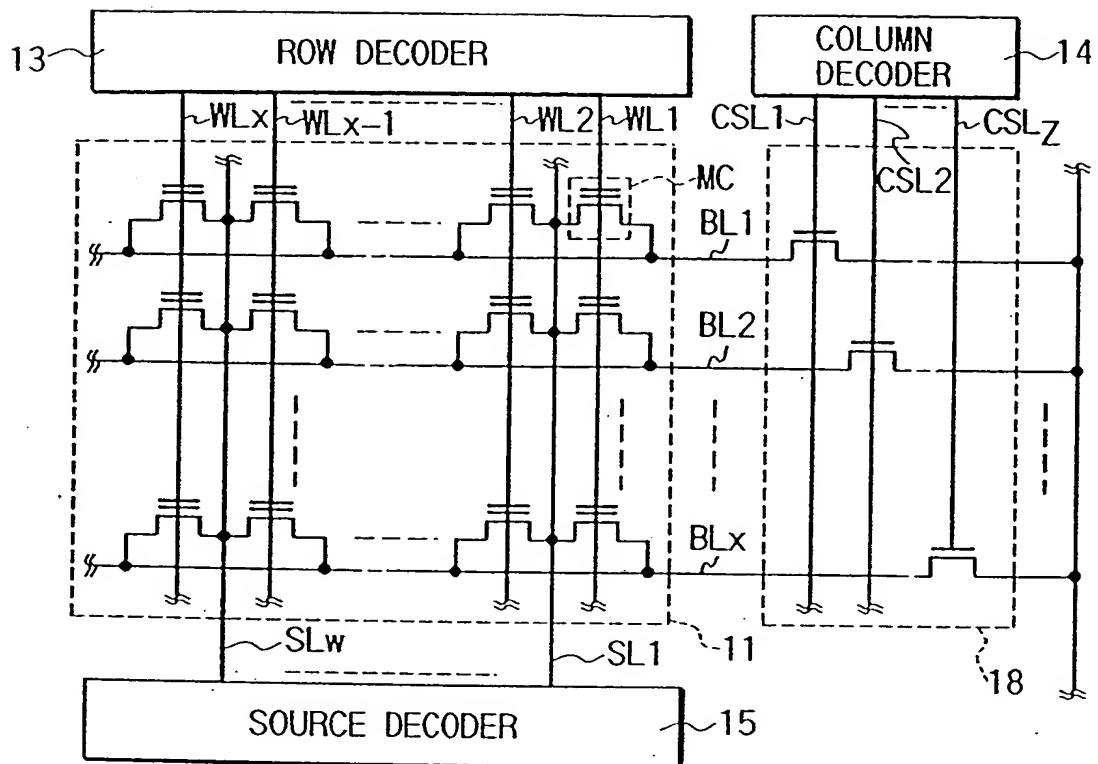


FIG. 8A

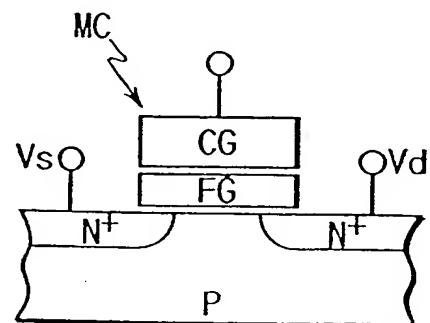


FIG. 8B

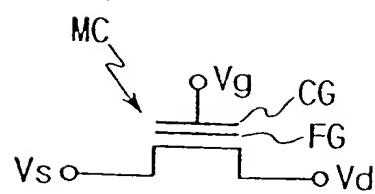


FIG. 8C

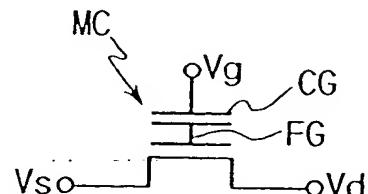


FIG. 8D

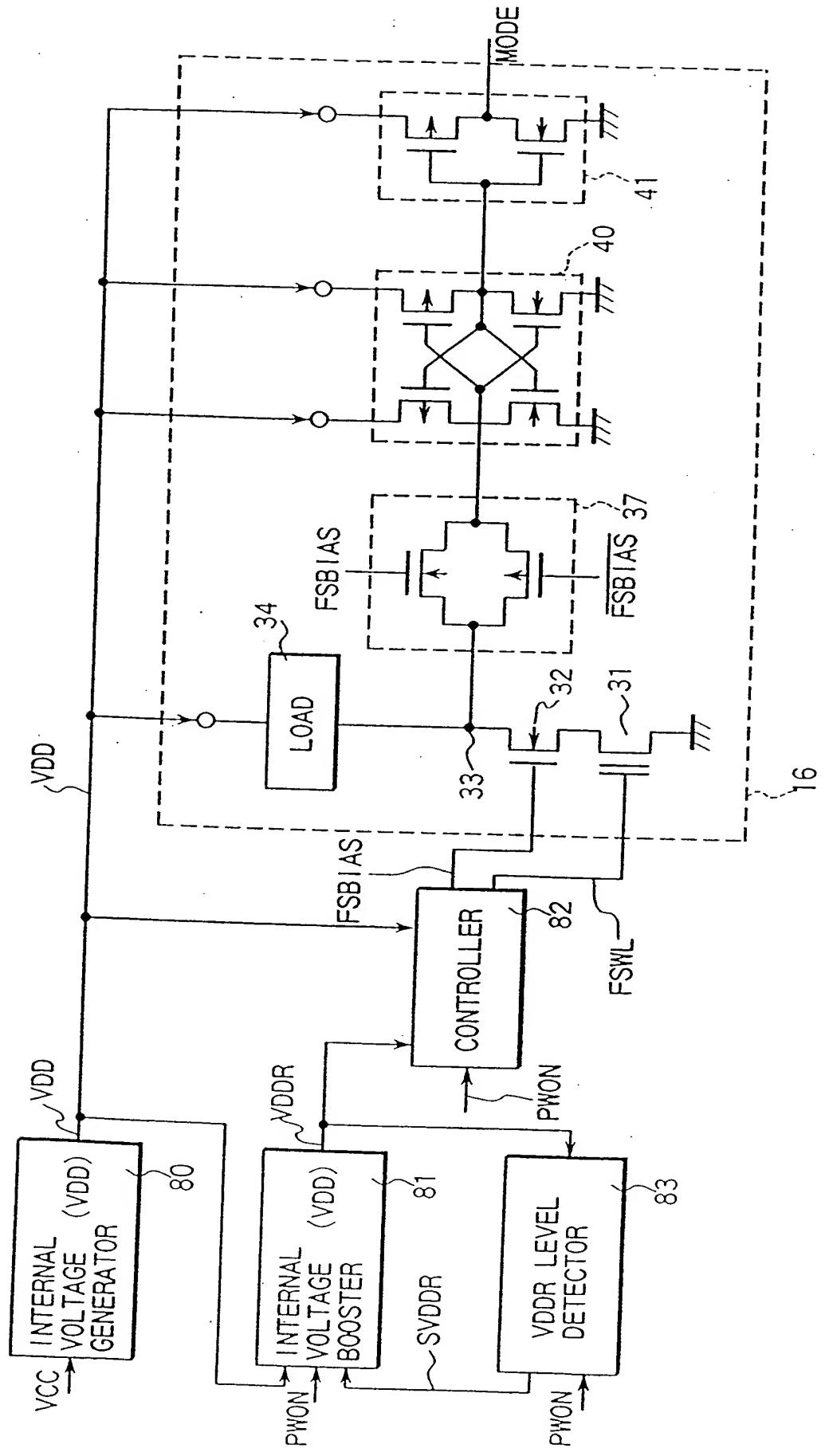
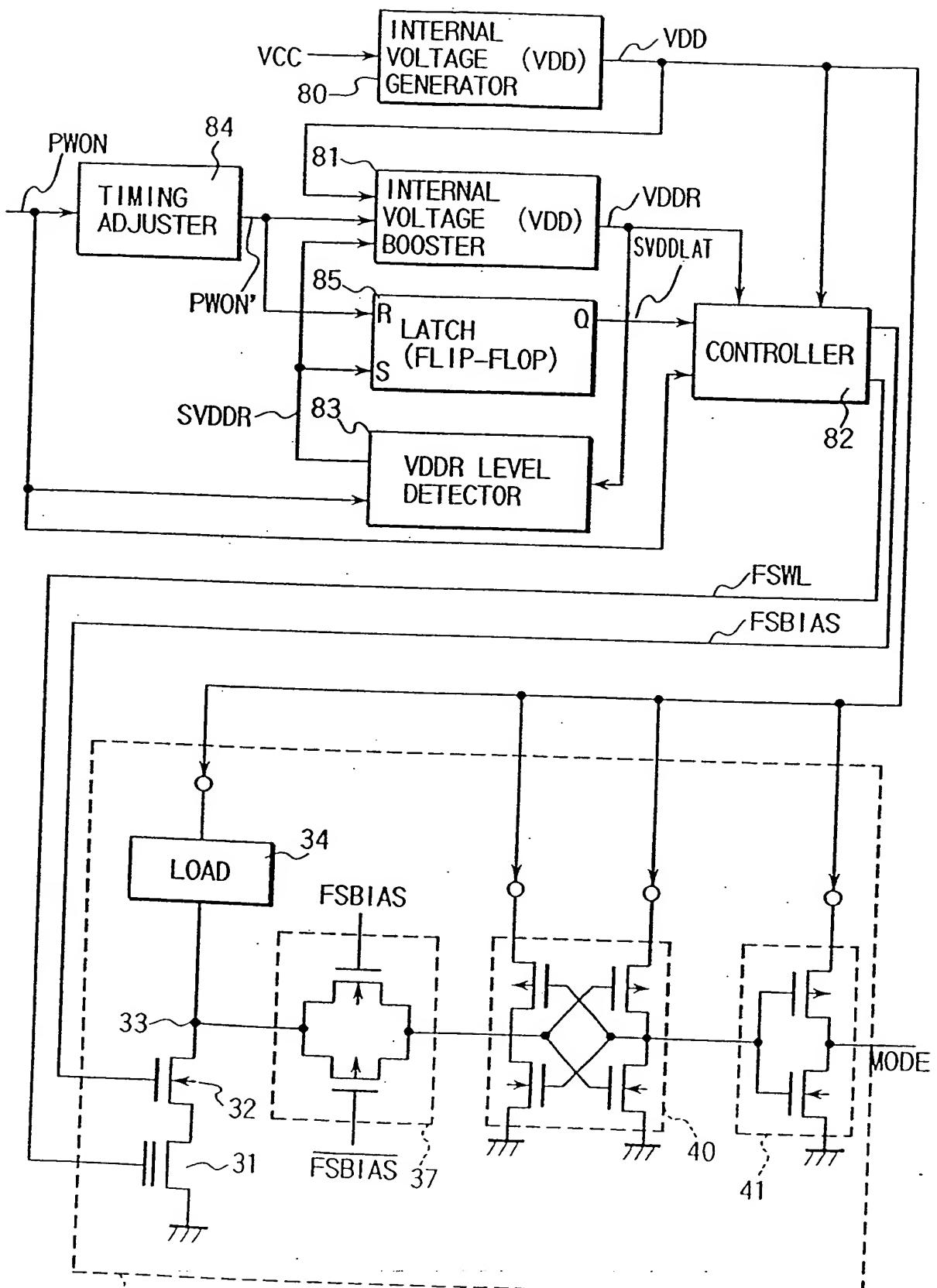


FIG. 9



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FIG. 10

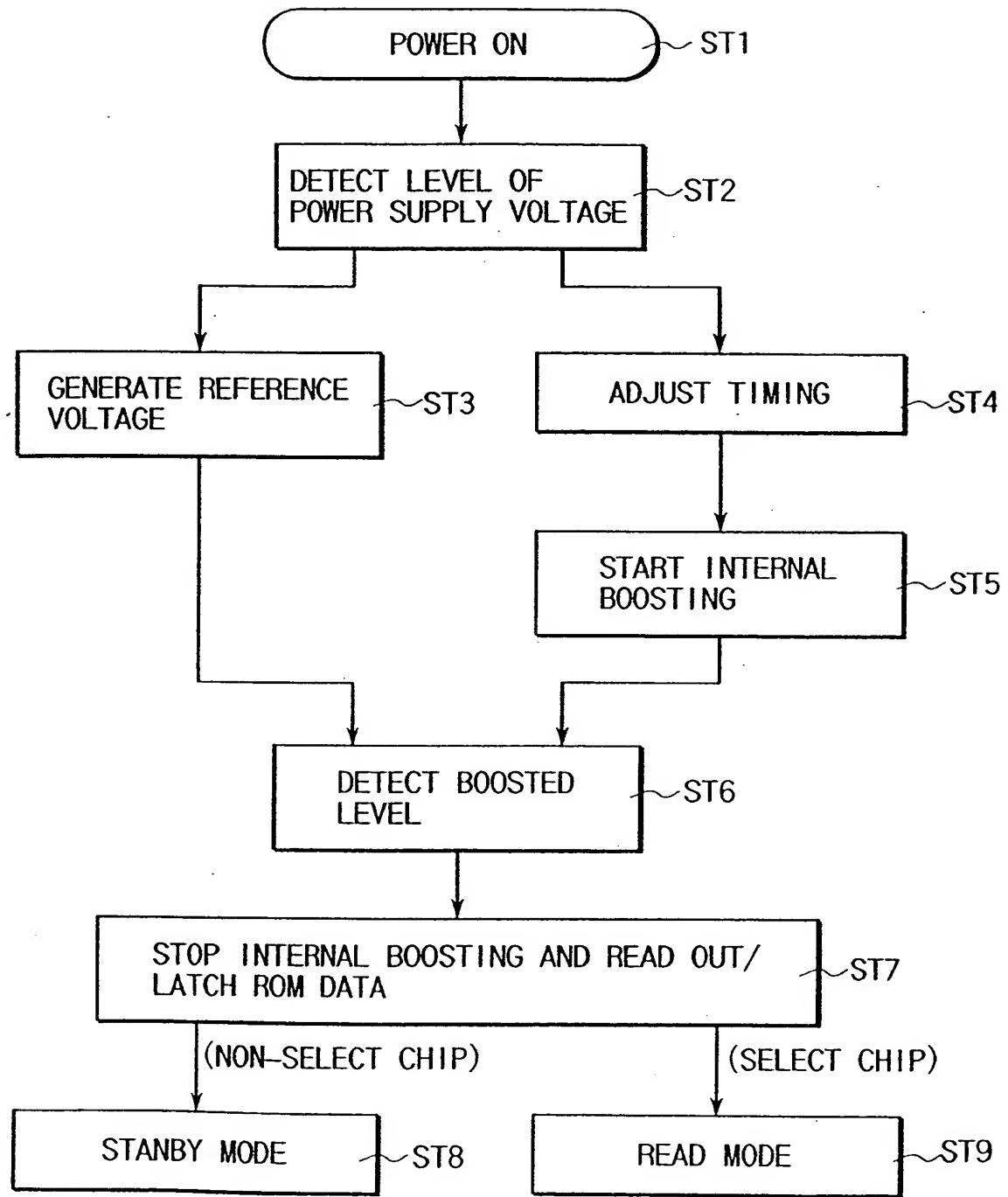


FIG.11

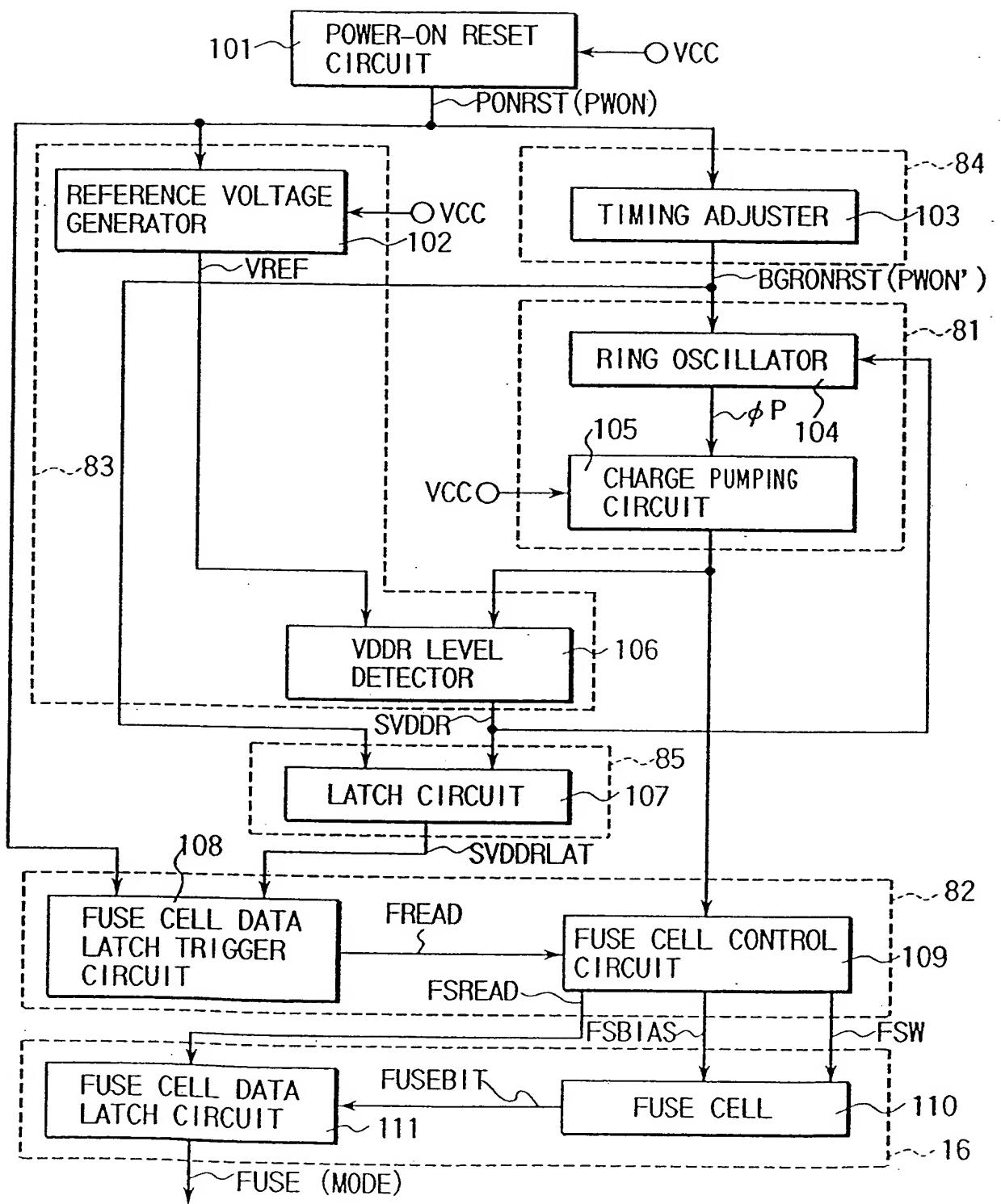
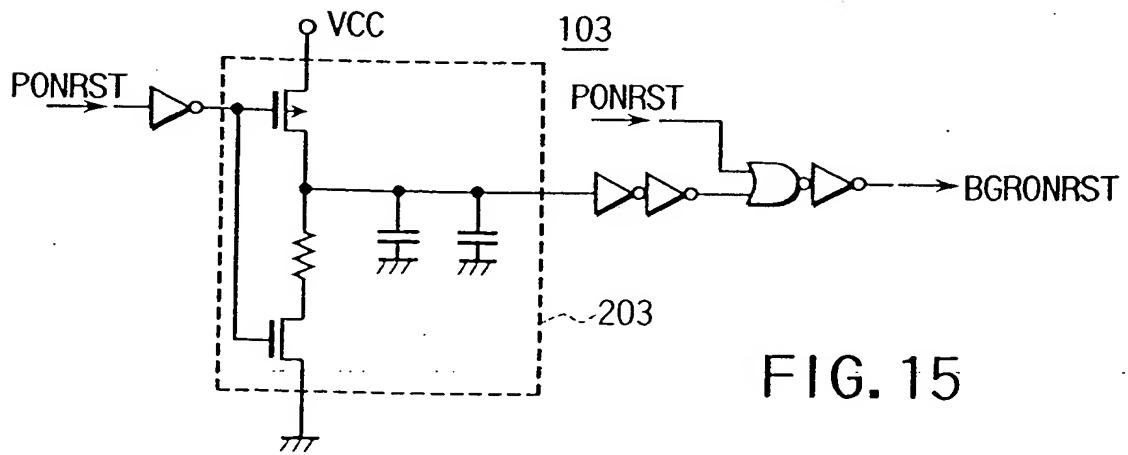
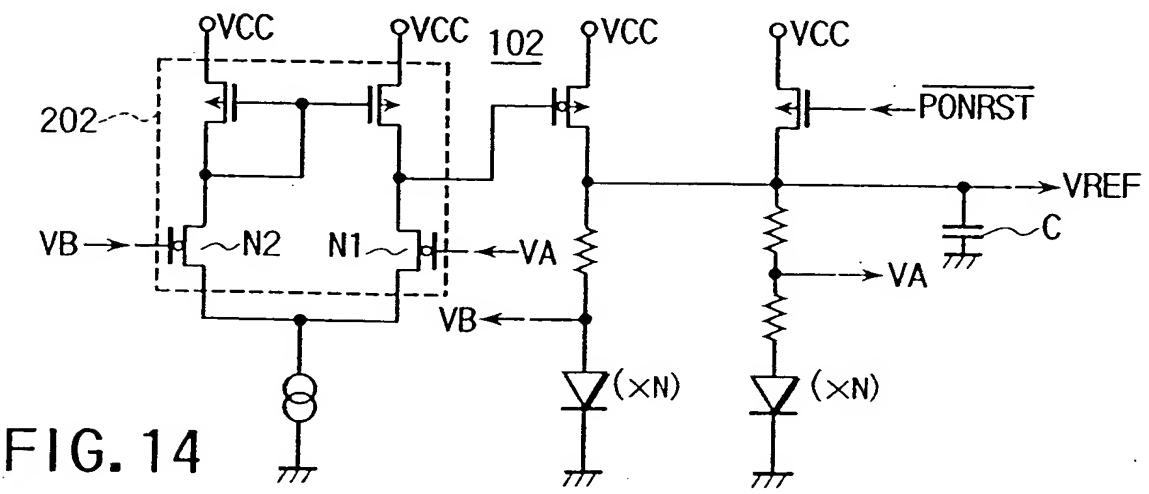
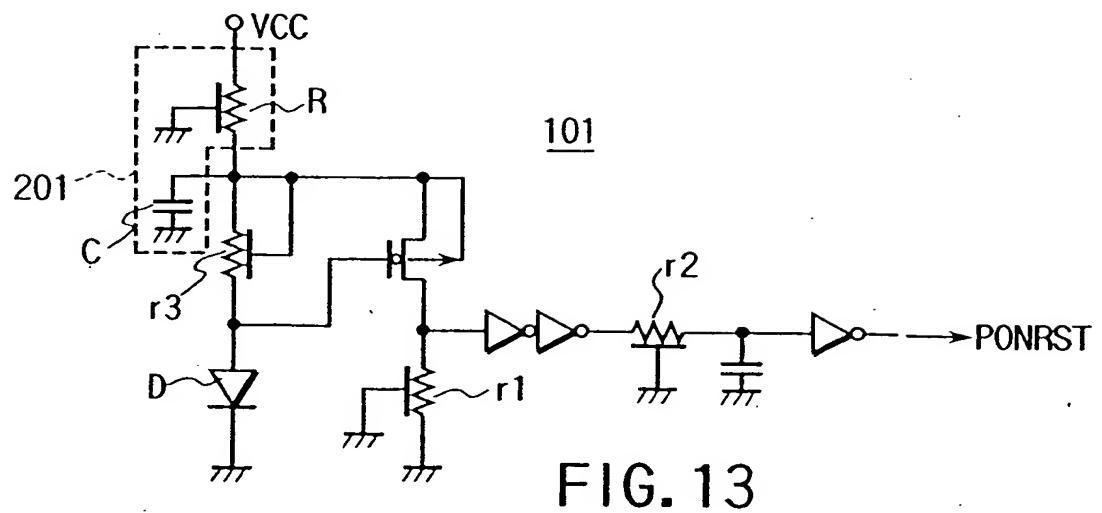
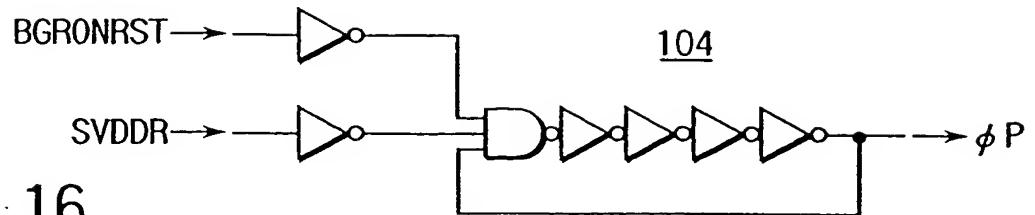
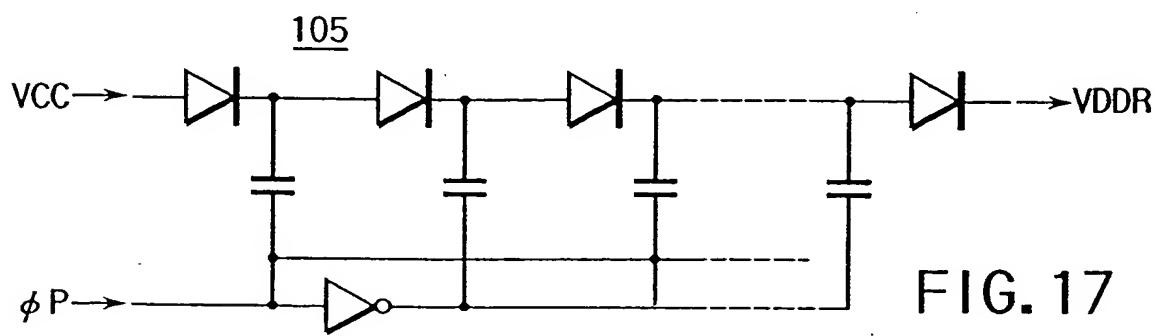


FIG. 12

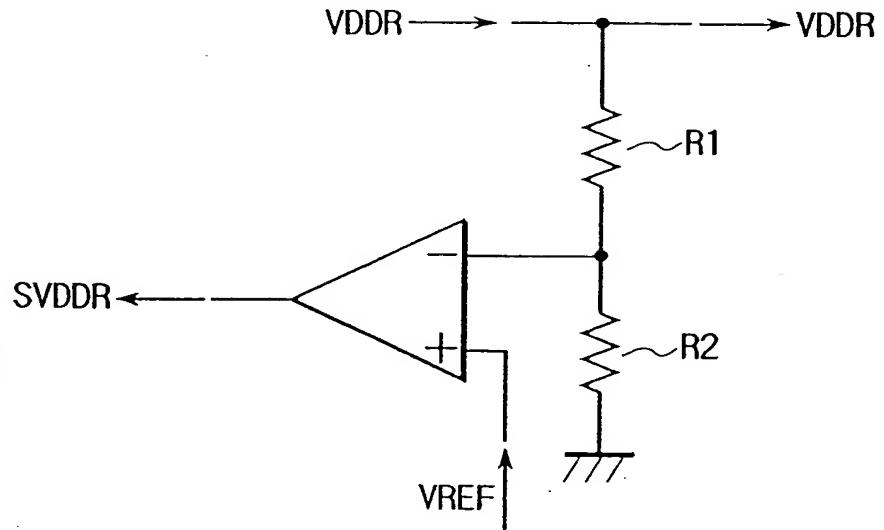




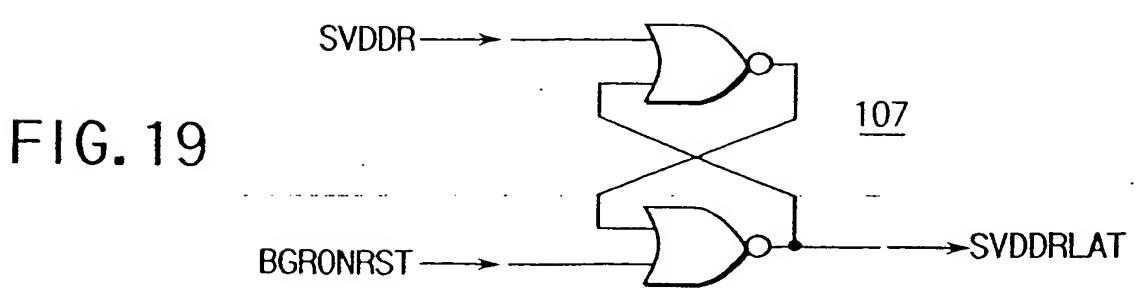
**FIG. 16**



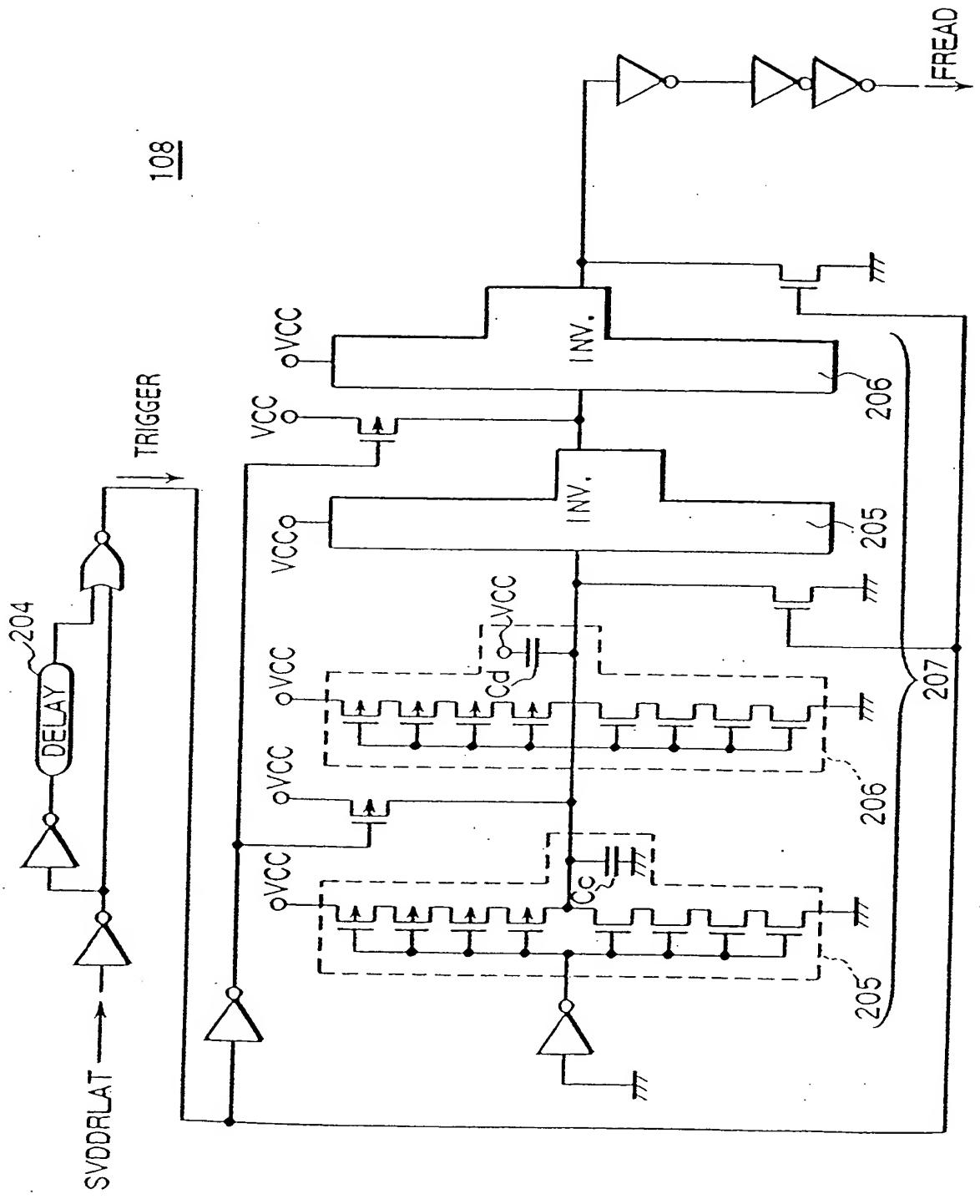
**FIG. 17**



**FIG. 18**



**FIG. 19**



E-G:20

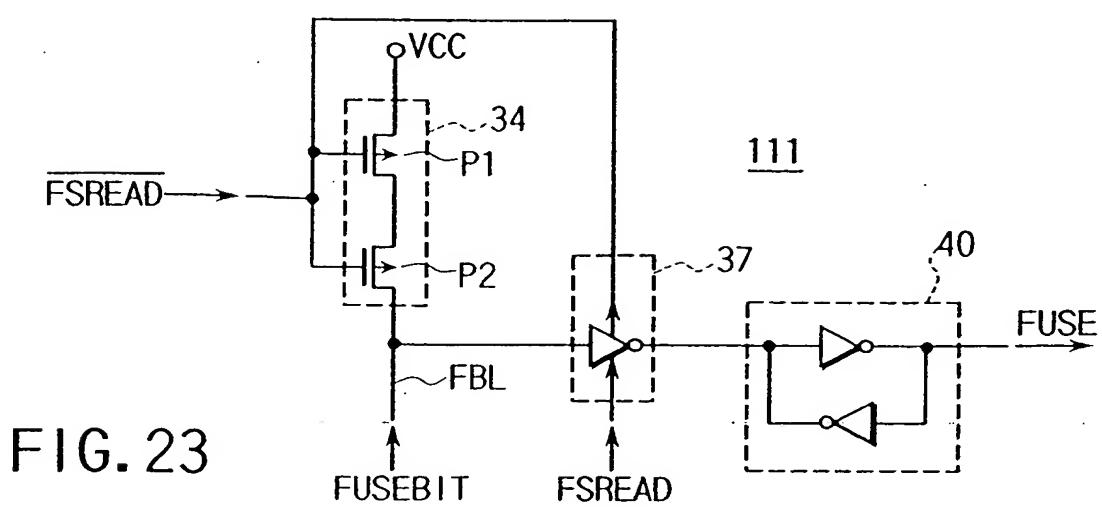
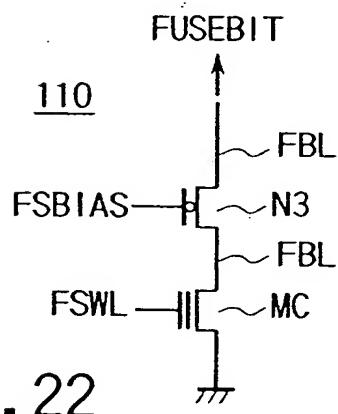
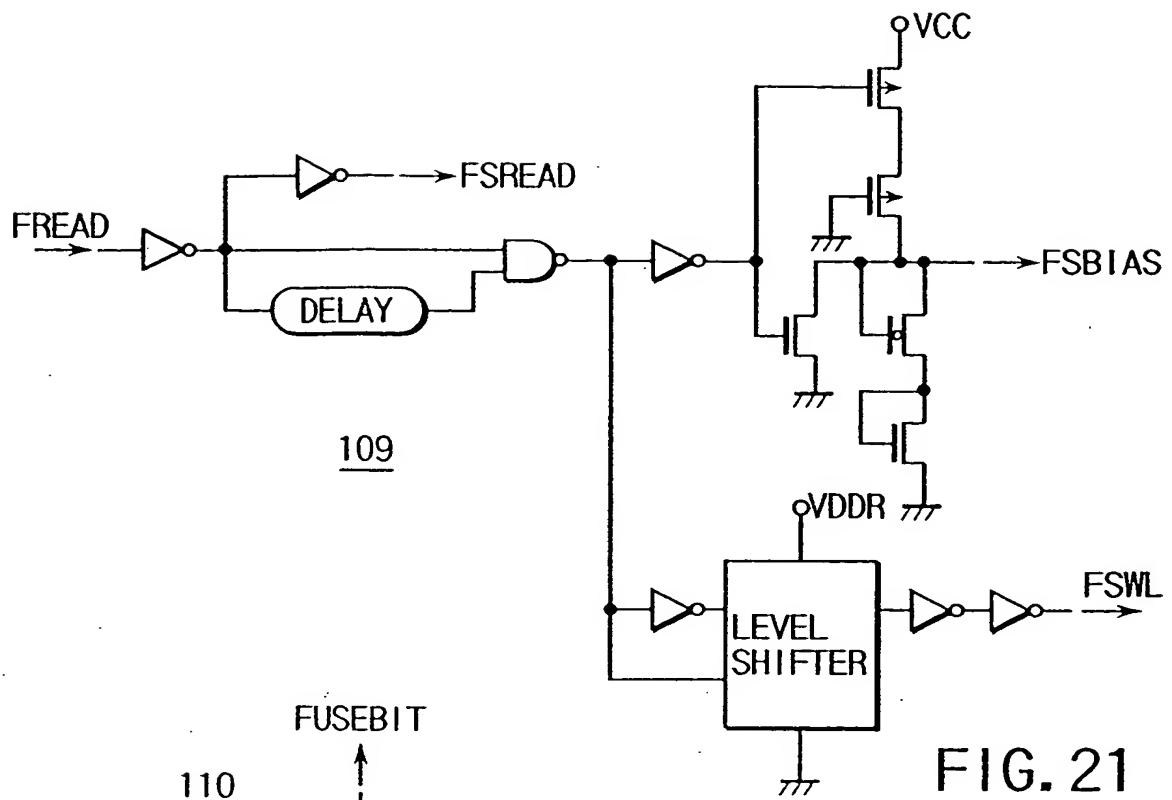
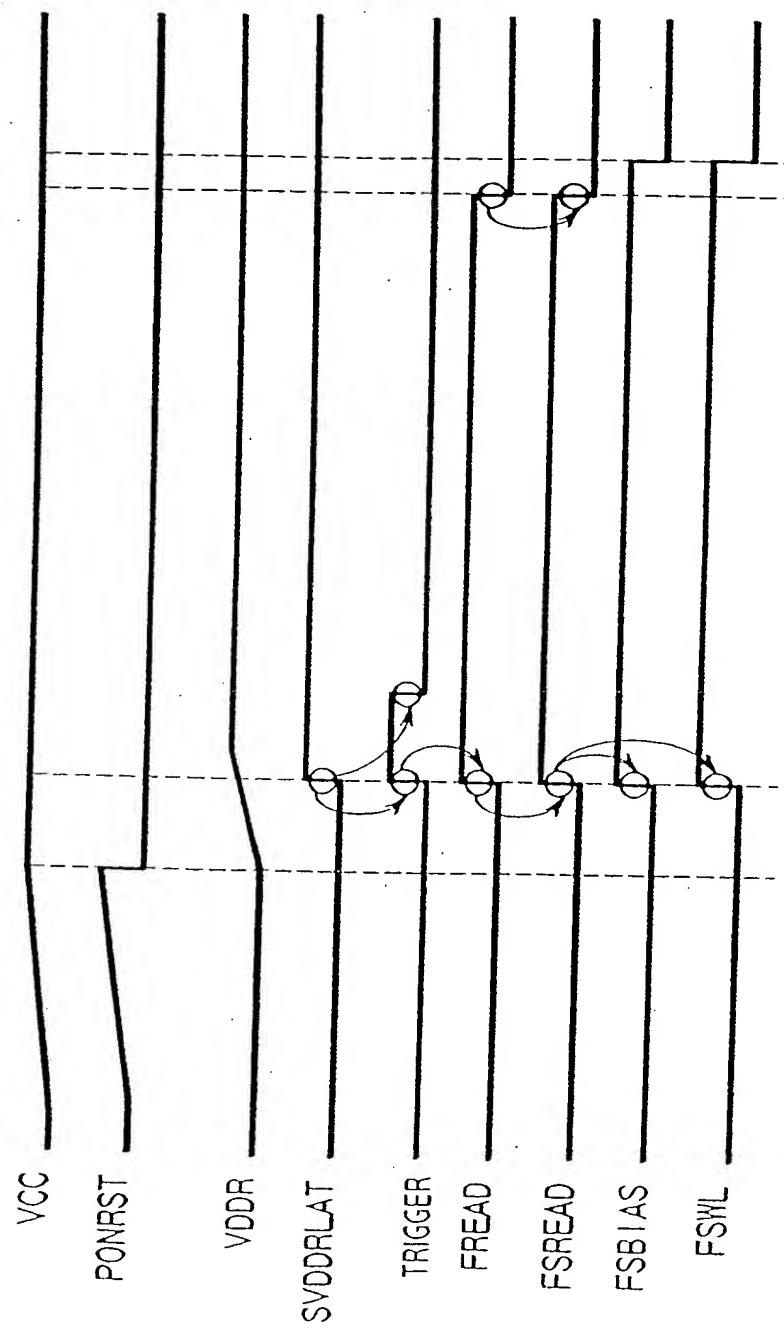


FIG. 24



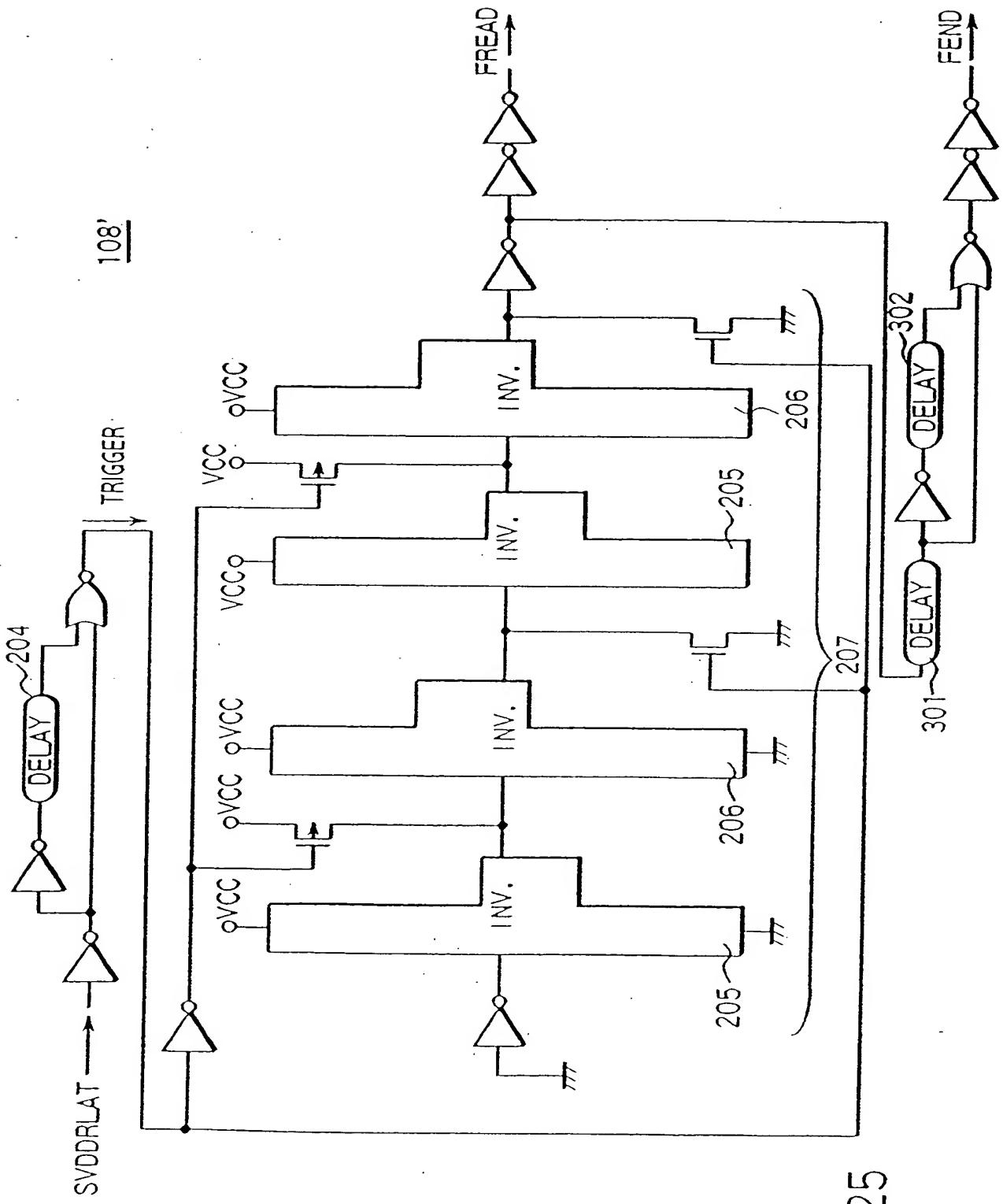
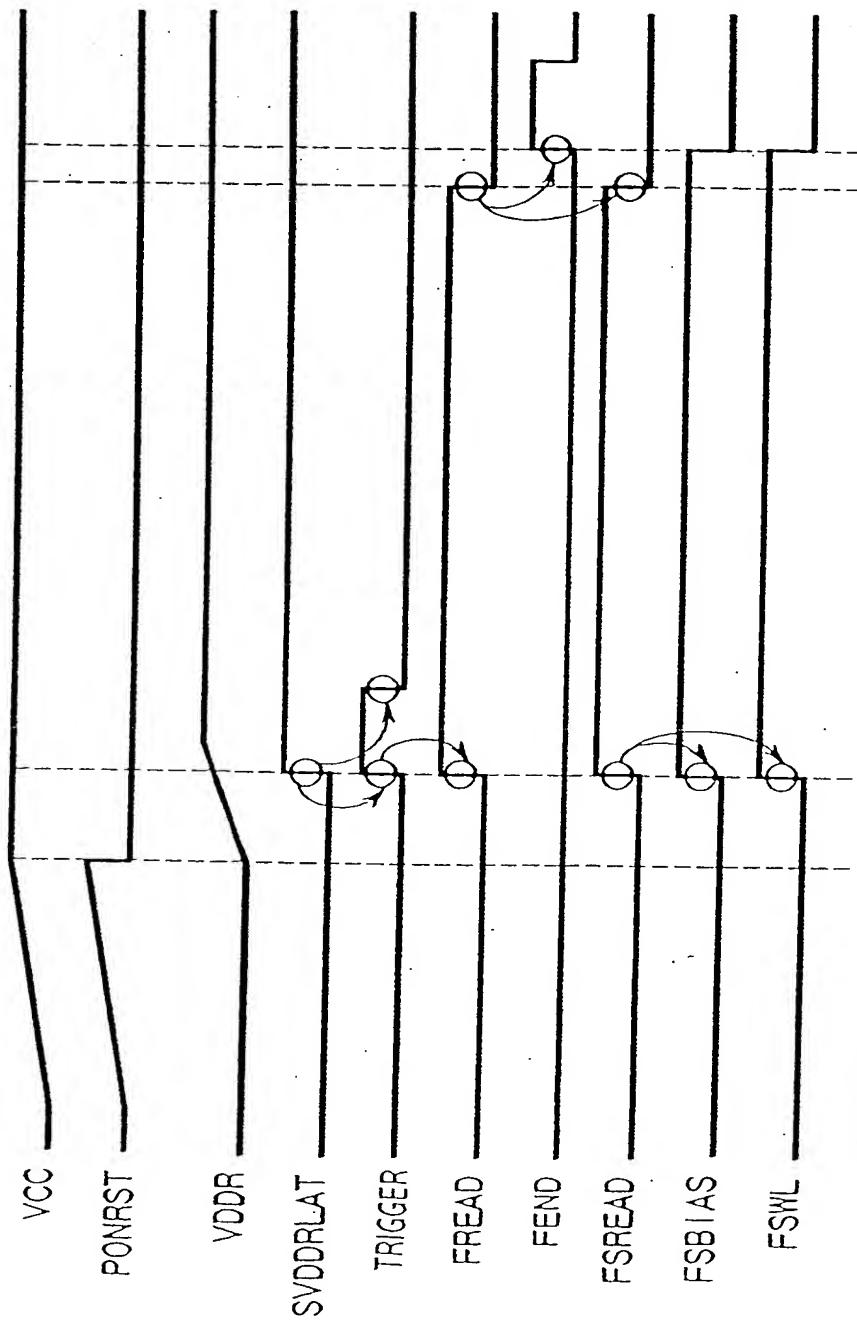


FIG. 25

FIG. 26



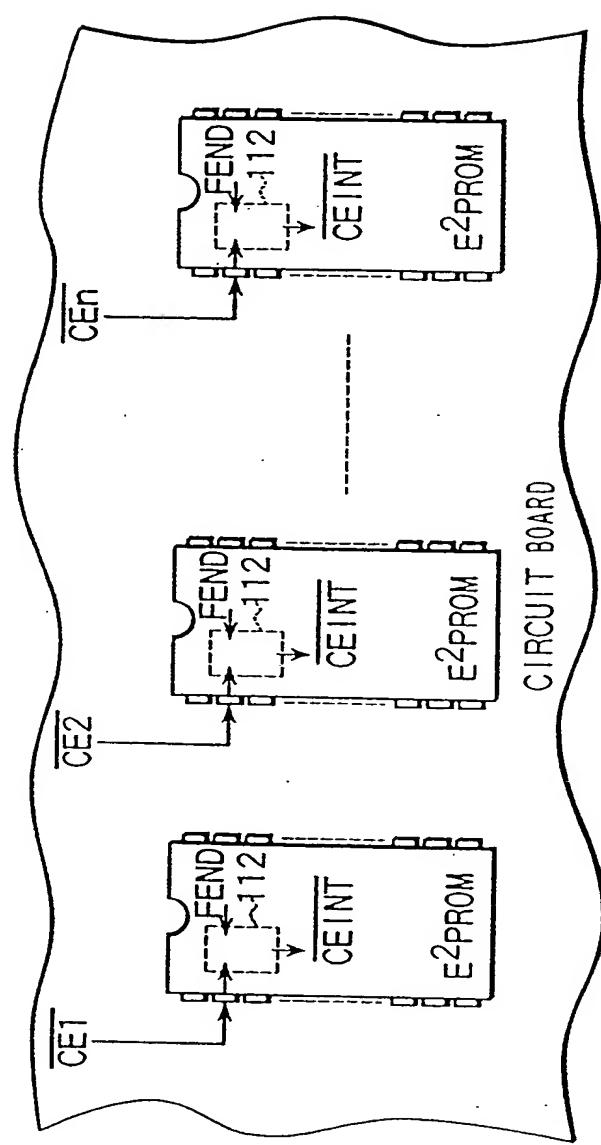


FIG. 27A

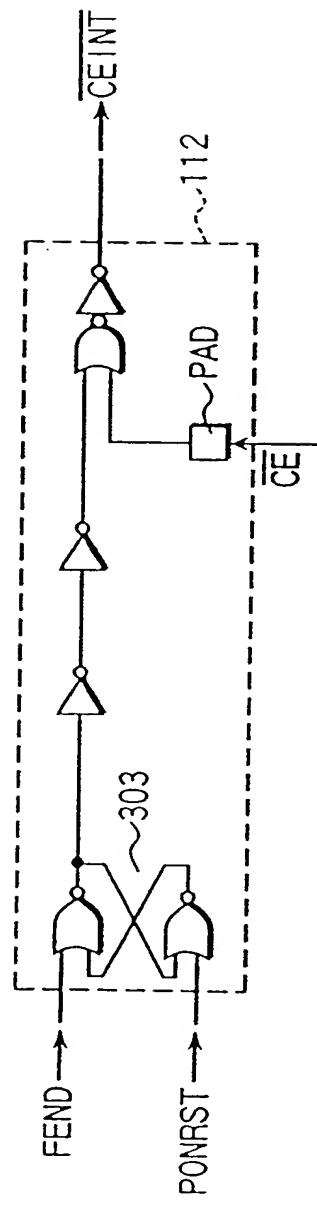
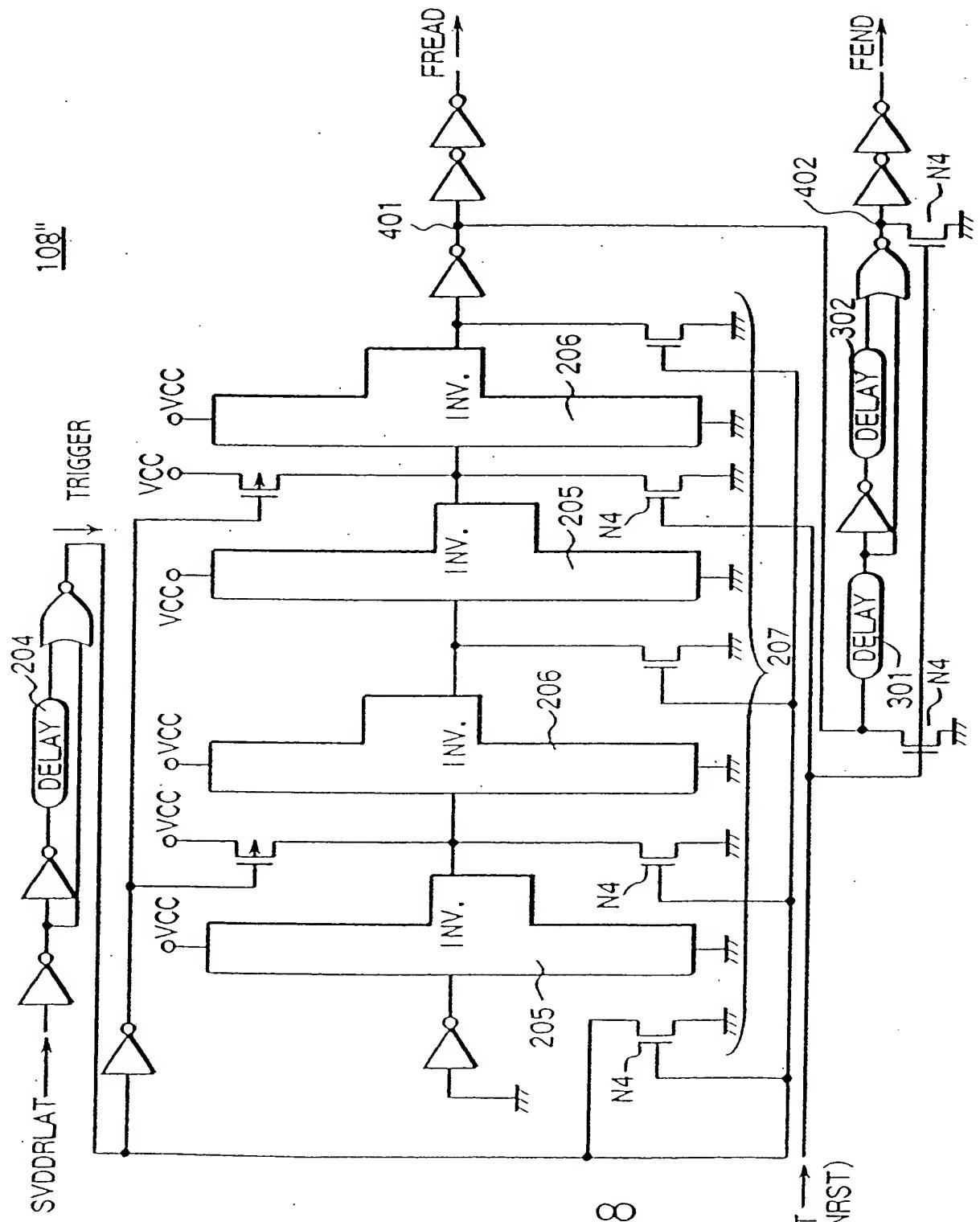
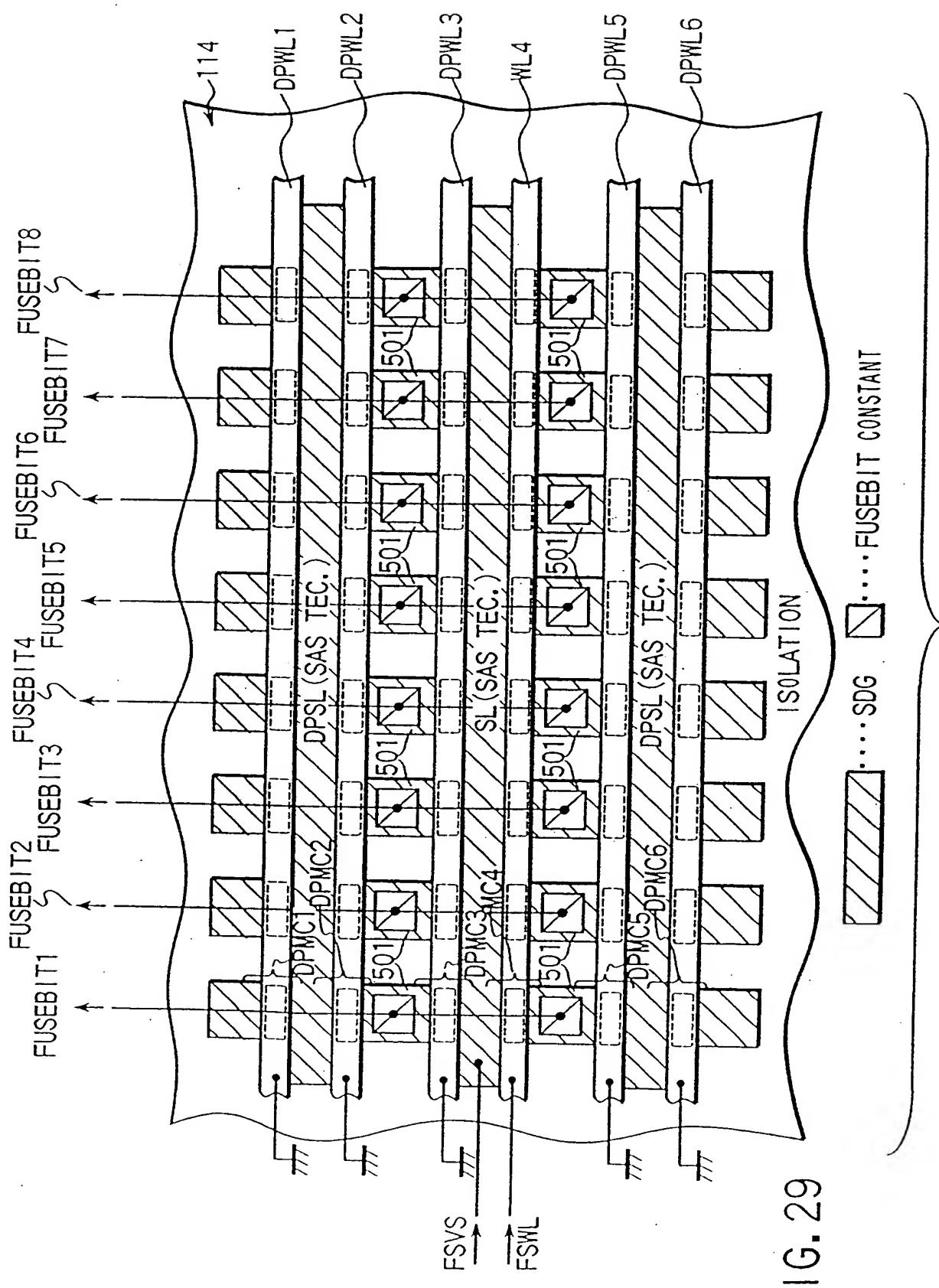


FIG. 27B.





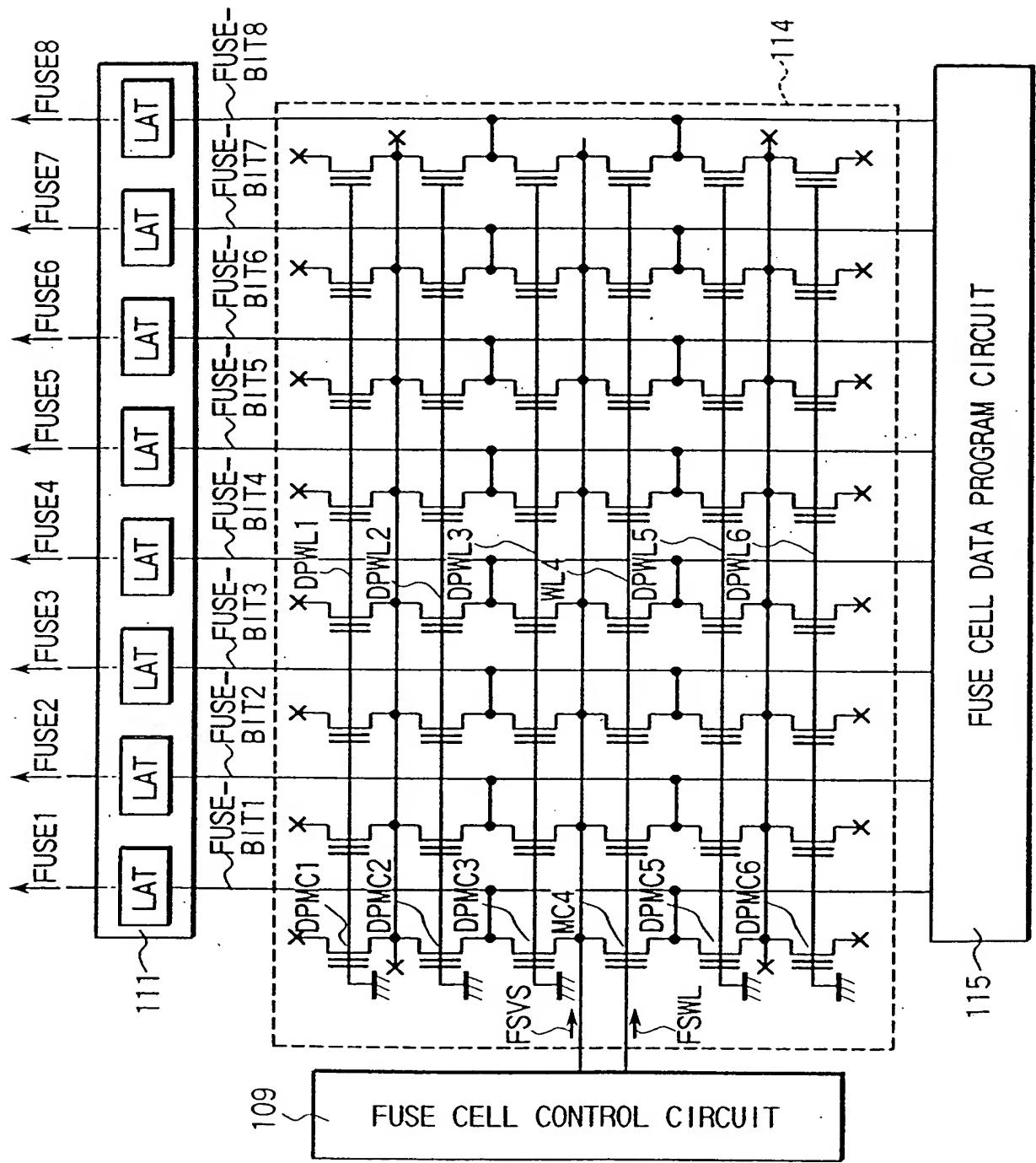


FIG. 30

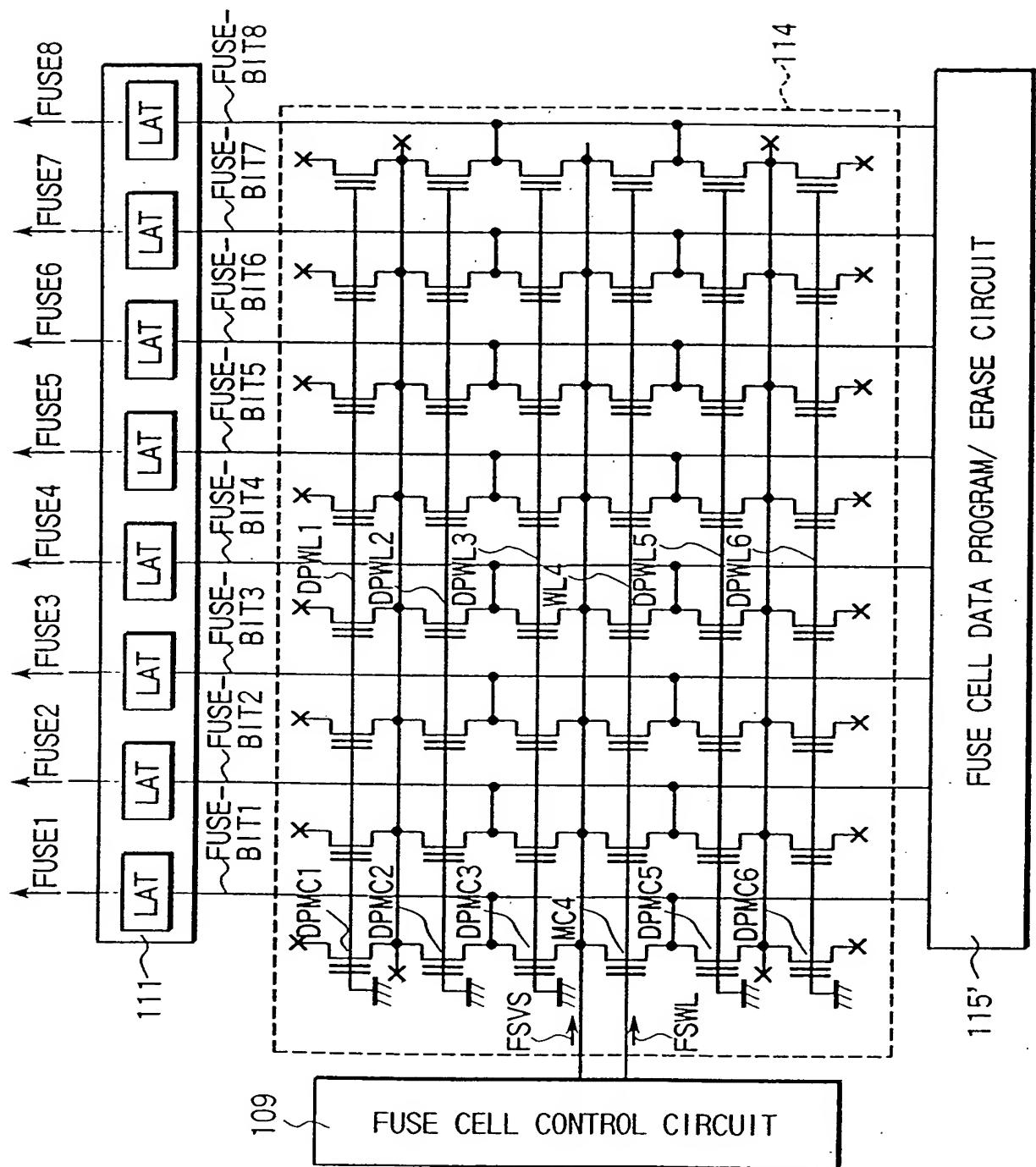


FIG. 31

FUSE CELL DATA PROGRAM/ ERASE CIRCUIT

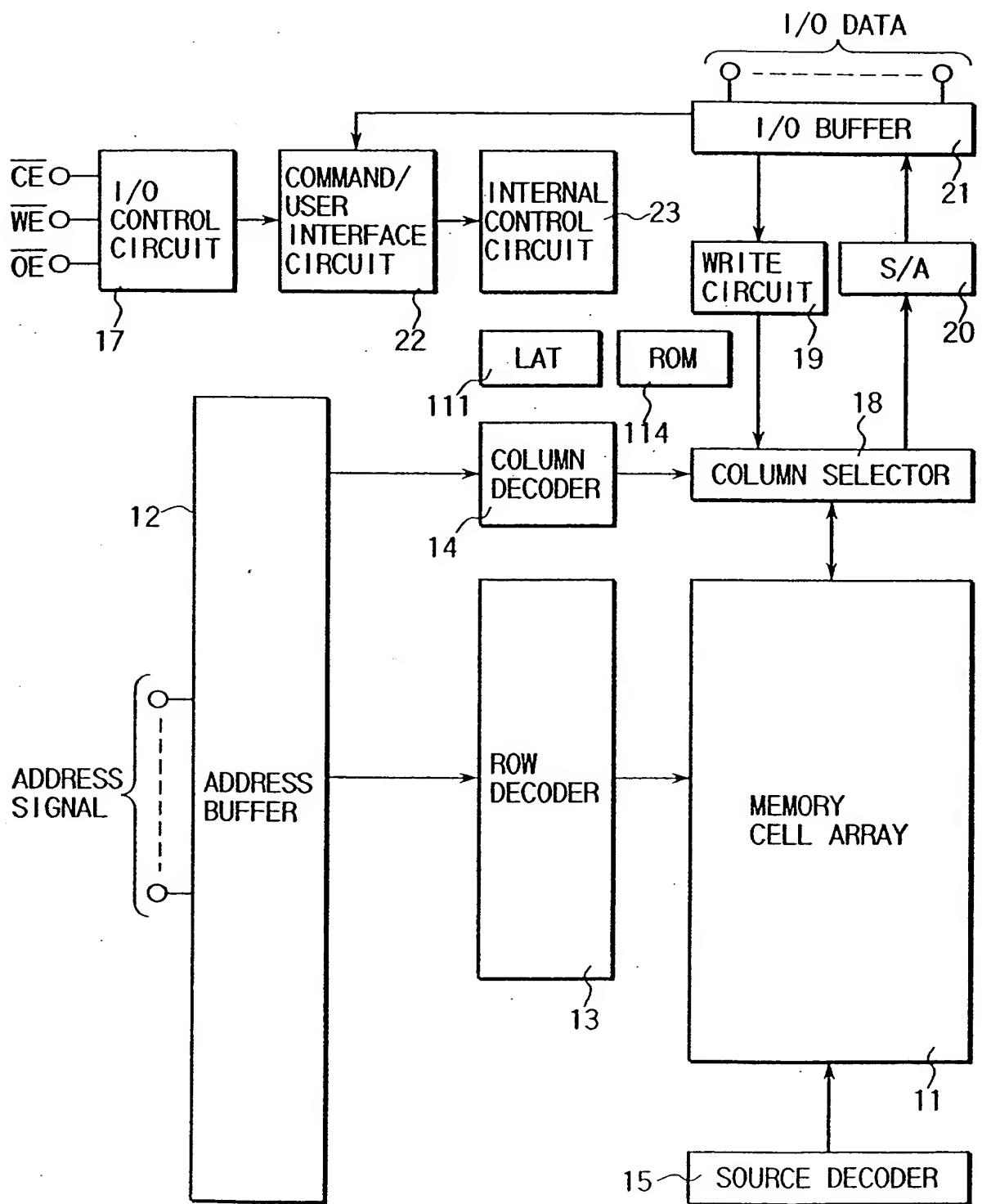


FIG. 32

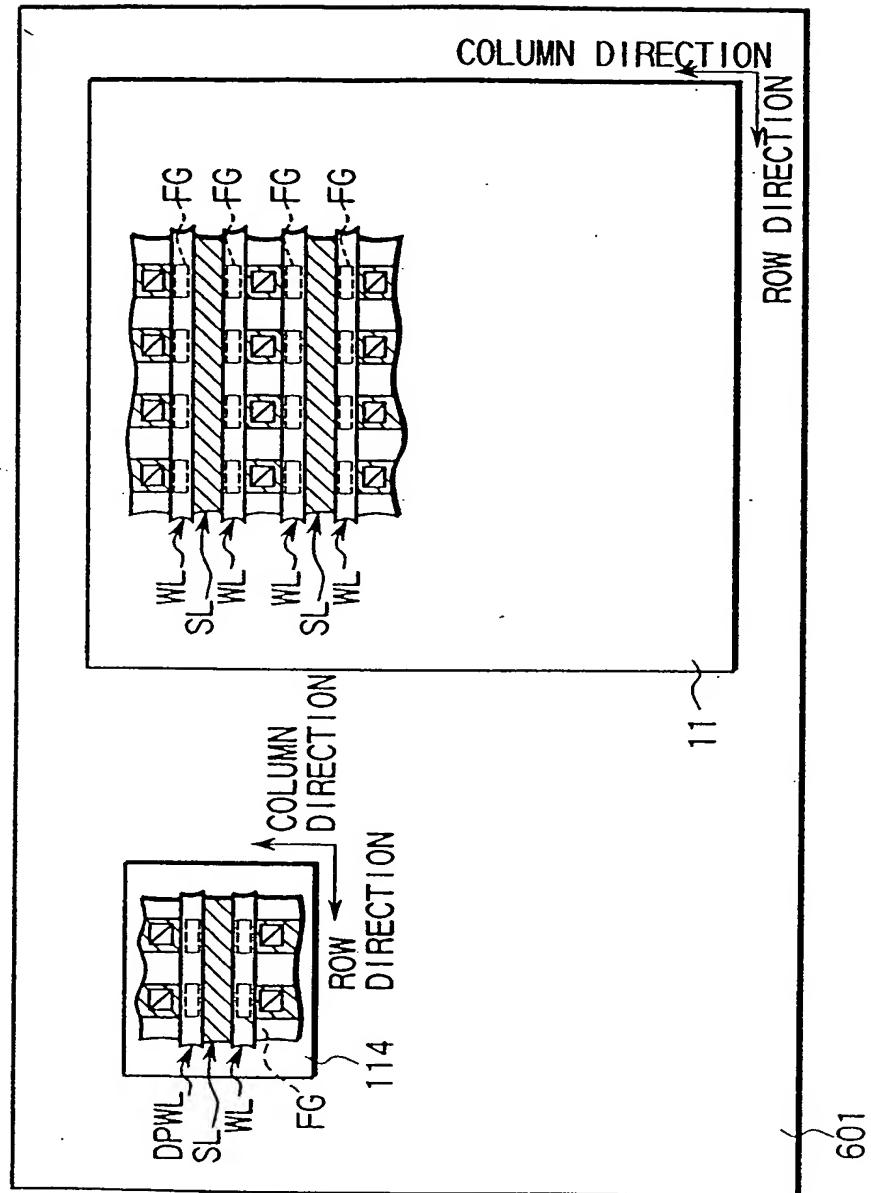


FIG. 33

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